

Introduction to Intel® Software Guard Extensions(Intel® SGX)

Cyberday 2015, Technion, Israel Ittai.anati@intel.com July 2015

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•No computer system can be absolutely secure.

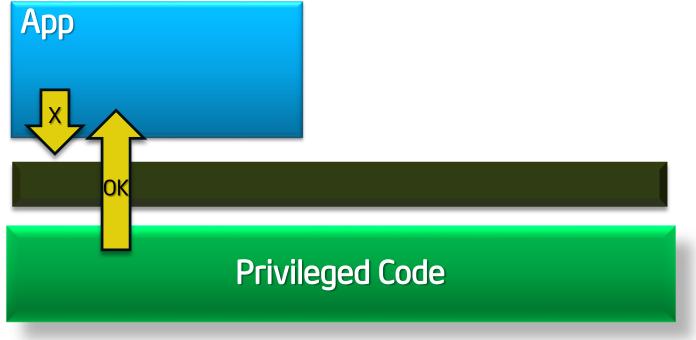


Slides are taken from the SGX tutorial slide deck from ISCA 2015, published at:

https://software.intel.com/en-us/isa-extensions/intel-sgx

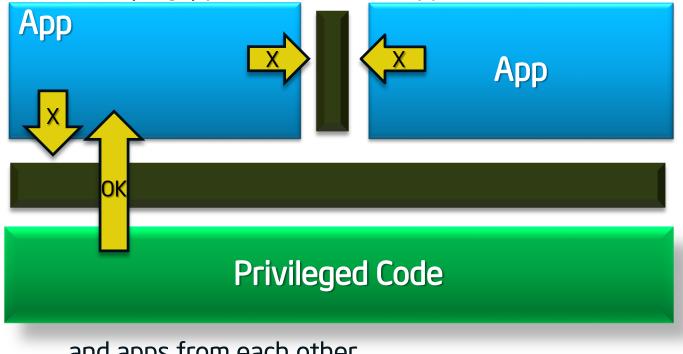


Protected Mode (rings) protects OS from apps ...





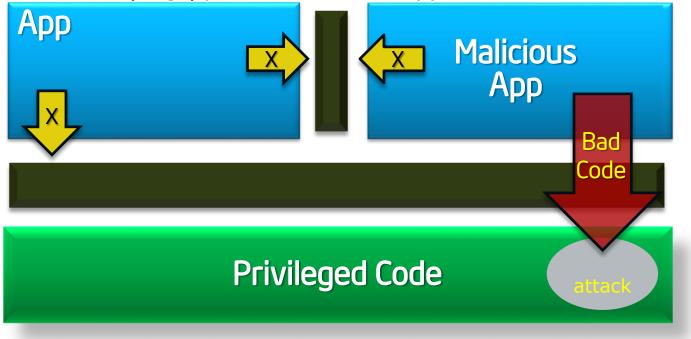
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... and apps from each other ...



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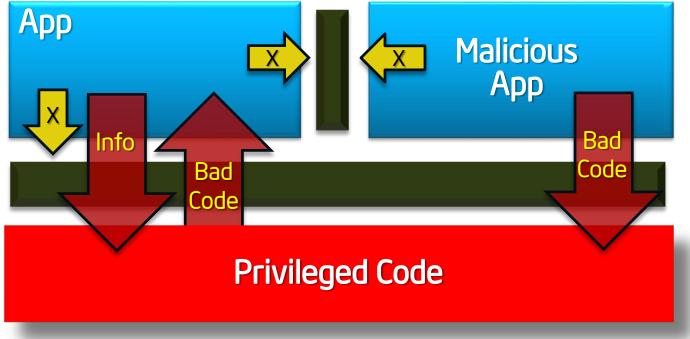
... and apps from each other ...

... UNTIL a malicious app exploits a flaw to gain full privileges and then tampers with the OS or other apps

Apps not protected from privileged code attacks



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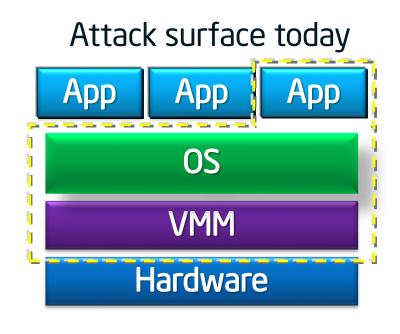


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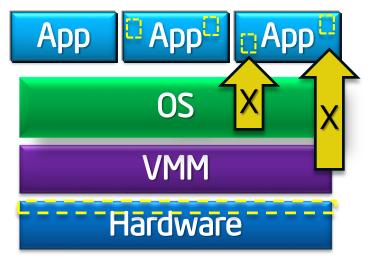




Application gains ability to defend its own secrets

- Small attack surface (App + processor)
- Malware that subverts OS/VMM, BIOS, Drivers etc. cannot steal app secrets

Attack surface with Enclaves







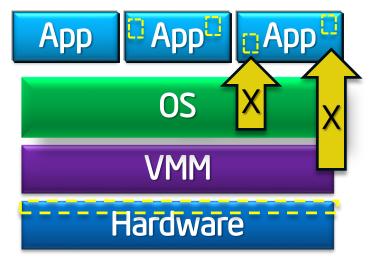
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Familiar development/debug

- Single application environment
- Build on existing ecosystem expertise

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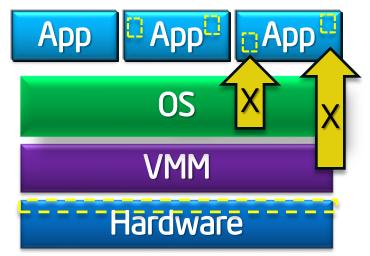
Familiar development/debug

- Single application environment
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Familiar deployment model

 Platform integration not a bottleneck to deployment of trusted apps

Attack surface with Enclaves





Scalable security within mainstream environment

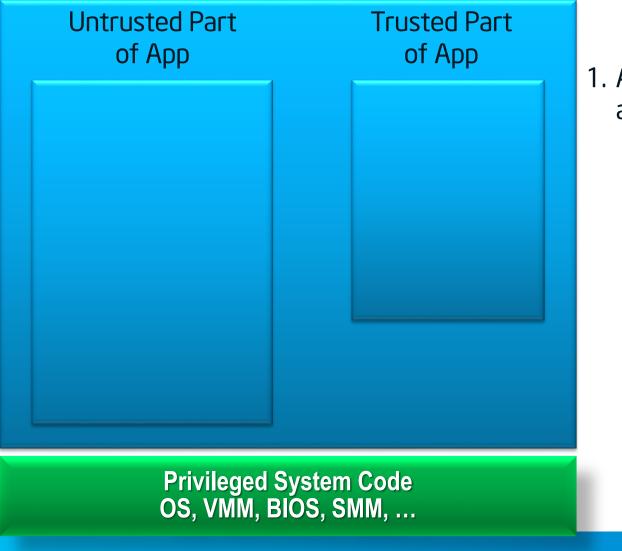


Application

Privileged System Code OS, VMM, BIOS, SMM, ...



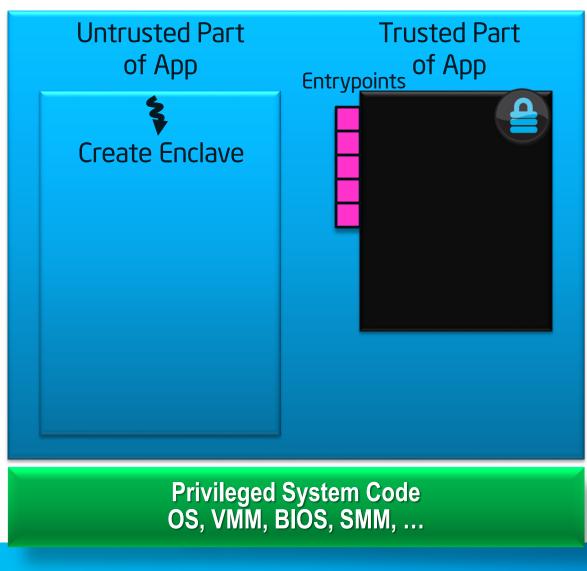
Application



1. App is built with trusted and untrusted parts



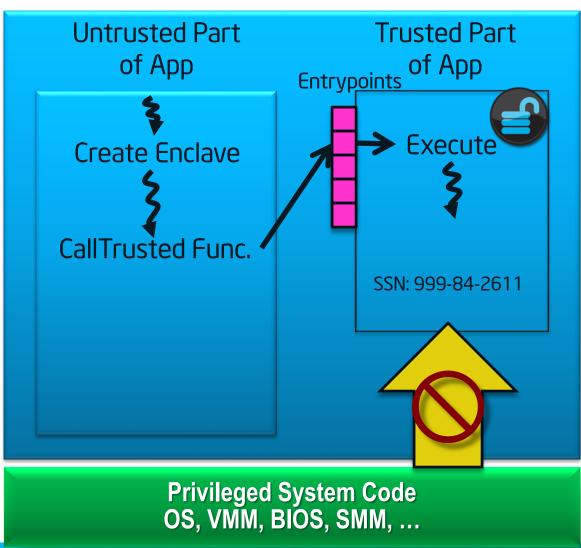
Application



- 1. App is built with trusted and untrusted parts
- 2. App runs & creates enclave which is placed in trusted memory



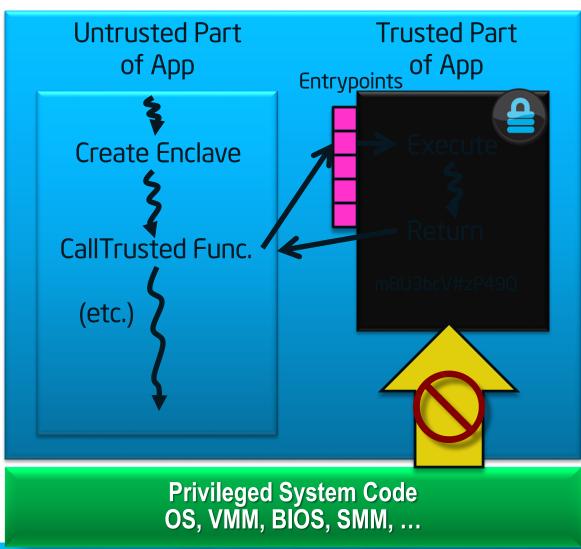
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- Trusted function is called; code running inside enclave sees data in clear; external access to data is denied



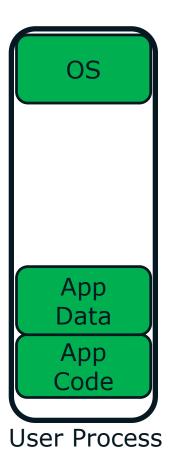
Application



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- 2. App runs & creates enclave which is placed in trusted memory
- Trusted function is called; code running inside enclave sees data in clear; external access to data is denied
- 4. Function returns; enclave data remains in trusted memory

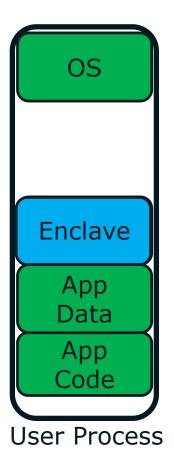


Trusted execution environment embedded in a process



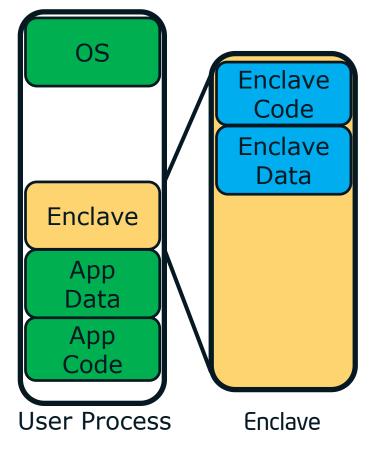


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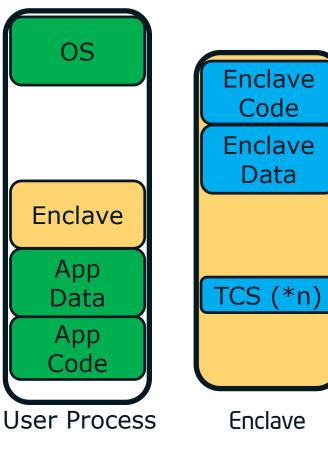
Trusted execution environment embedded in a process



With its own code and data Provide Confidentiality Provide integrity With controlled entry points



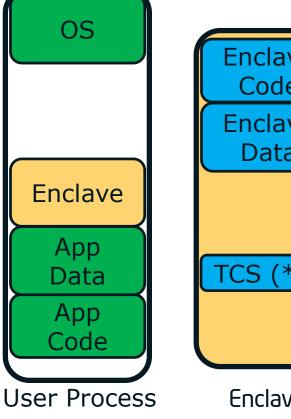
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With its own code and dataProvide ConfidentialityProvide integrityWith controlled entry pointsSupporting multiple threads



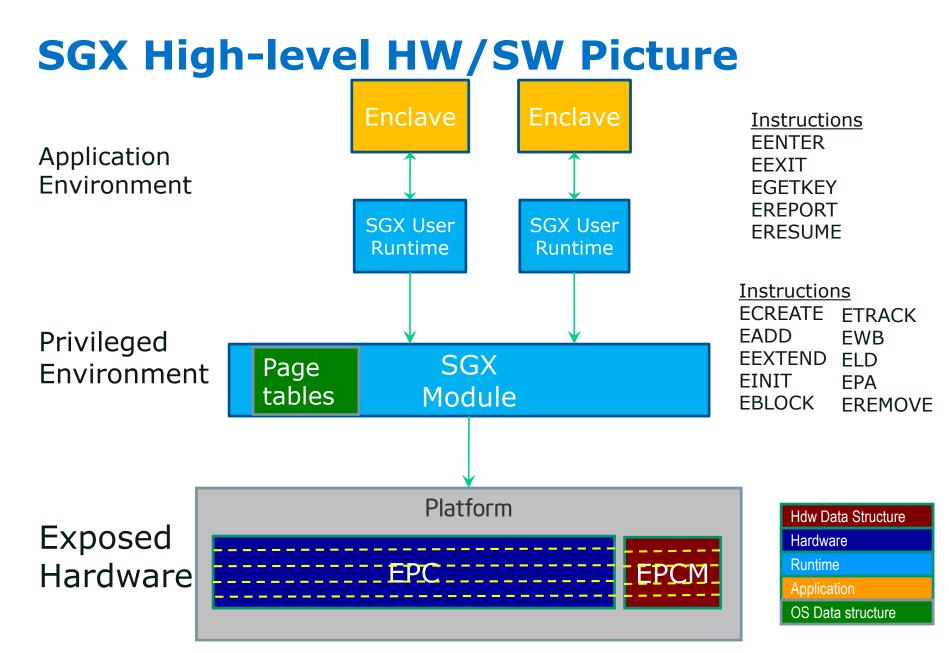
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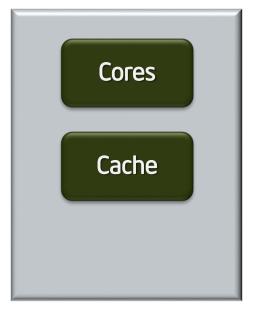
Enclave Code Enclave Data TCS (*n) Enclave

With its own code and data Provide Confidentiality Provide integrity With controlled entry points Supporting multiple threads With full access to app memory



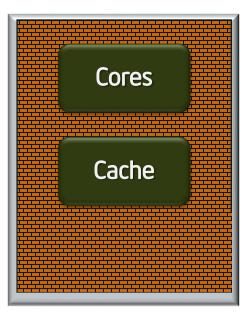










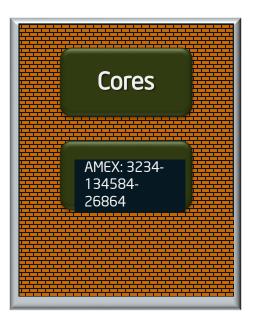




Non-Enclave Access

Security perimeter is the CPU package boundary

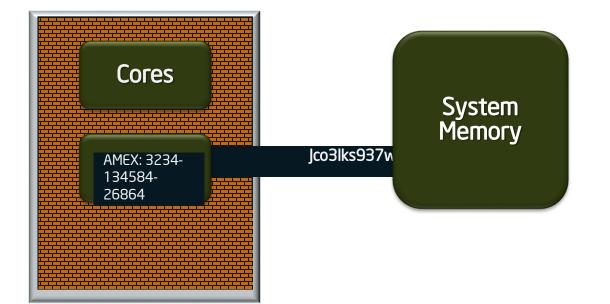






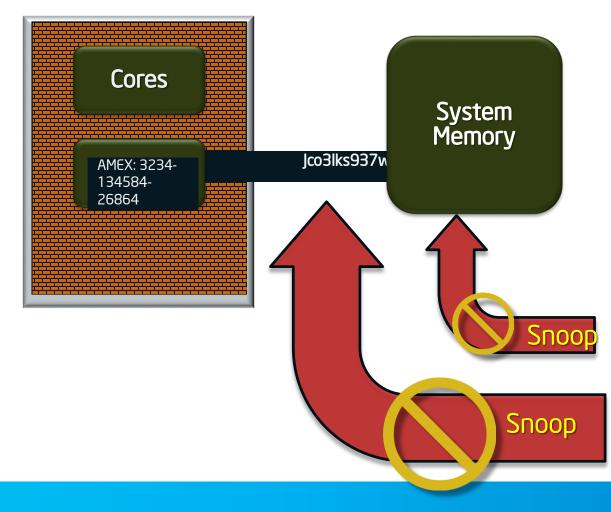
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- Data and code unencrypted inside CPU package





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- Data and code outside CPU package is encrypted and integrity checked





- Security perimeter is the CPU package boundary
- Data and code unencrypted inside CPU package
- Data and code outside CPU package is encrypted and integrity checked
- External memory reads and bus snoops see only encrypted data



Client Application

Remote Platform







Remote Platform



1. Enclave built & measured







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- 2. Enclave requests REPORT (HW-signed blob that includes enclave identity information)

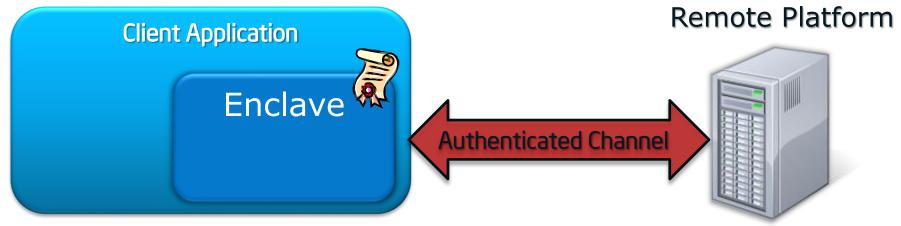






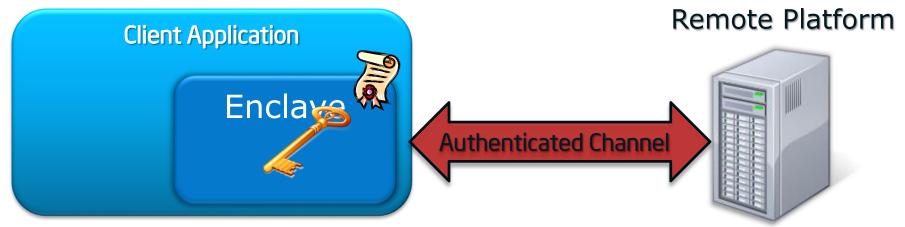
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- 3. REPORT sent to server & verified





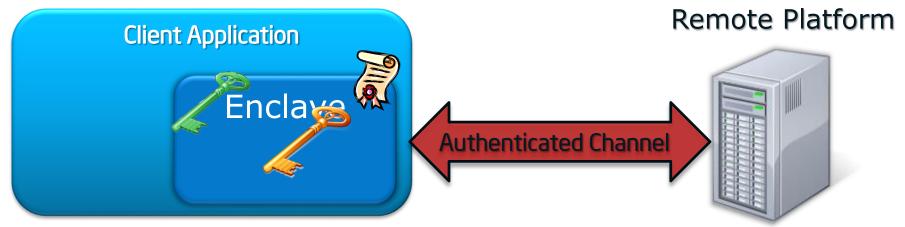
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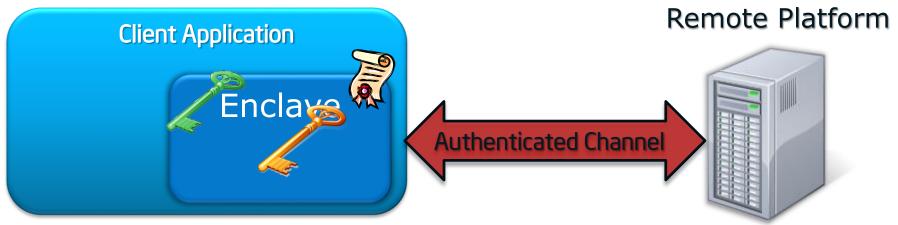
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- 5. Enclave-platform-specific Sealing Key generated (EGETKEY)





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- 3. REPORT sent to server & verified
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- 5. Enclave-platform-specific Sealing Key generated (EGETKEY)
- 6. Application Key encrypted via Sealing Key & stored for later (offline) use



Creating an Enclave - ISV

- Developer writes and compiles the enclave
 - Trusted functions at the enclave and rest outside
 - SGX1.0 will need to allocate all the memory upfront, SGX2.0 can dynamically allocate memory.
- Developer installs the enclave as DEBUG
 - Any enclave can be run as debug
 - Debug OPTIN is controlled per thread, and can be set via EDBGWR.



Creating SIGSTRUCT - ISV

- SIGSTRUCT Enclave Signature Structure
 - Used to measure the enclave and attributes.
- Measuring the enclave content using SHA-256
- Specifying the attributes
- Setting the ISV information
 - Product ID
 - Security version number ISVSVN
- Signing the App's SIGSTRUCT using the ISV private key with RSA-3072.

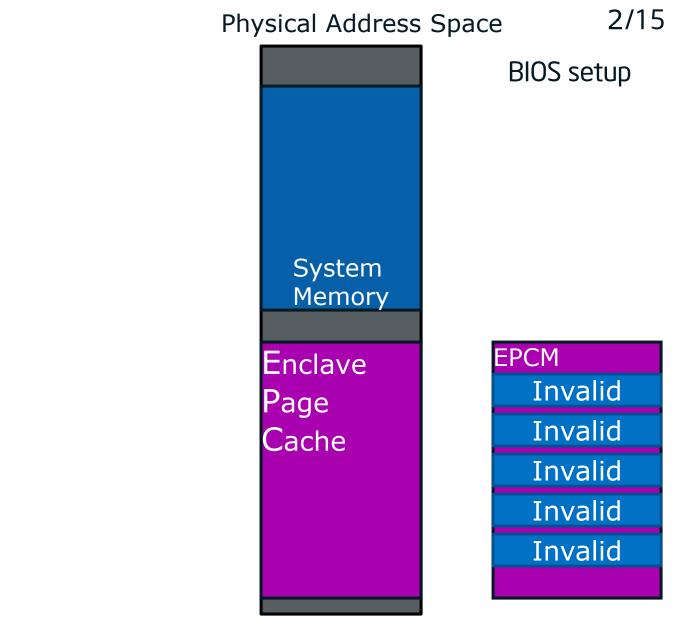


Physical Address Space

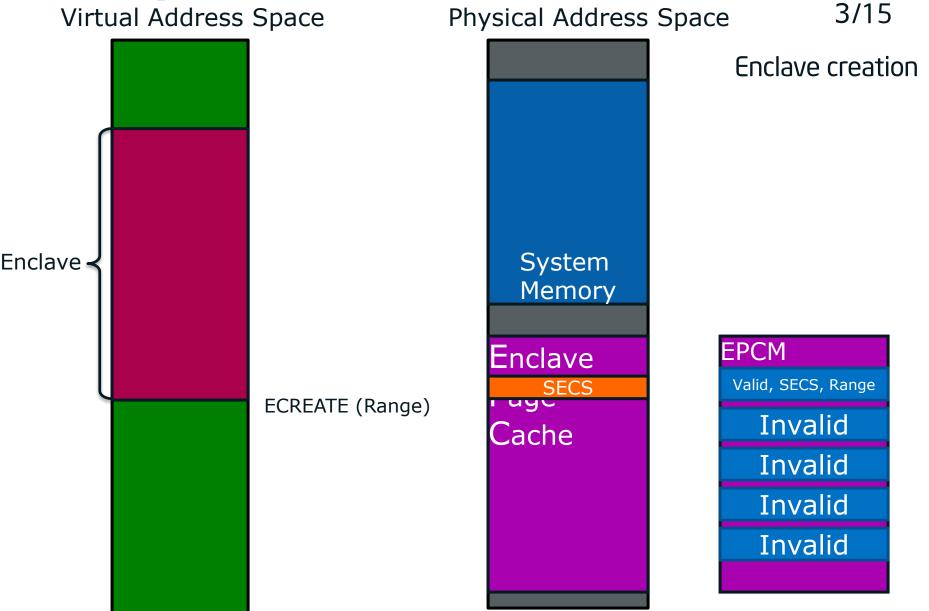
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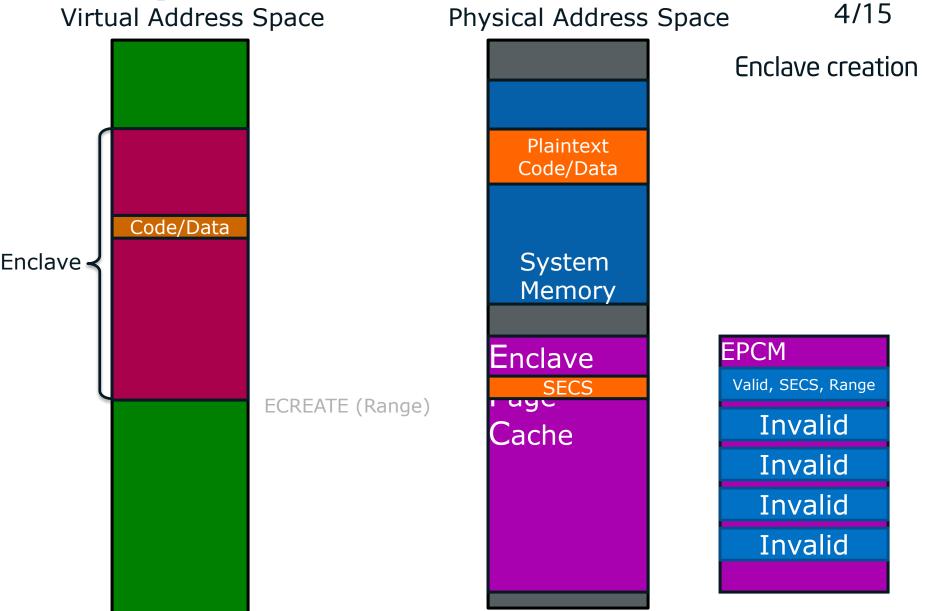




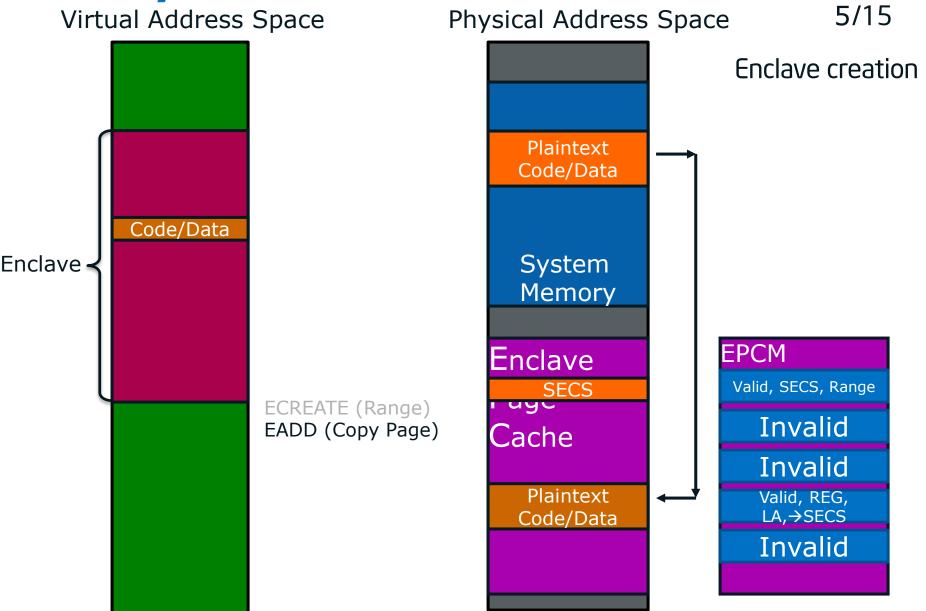




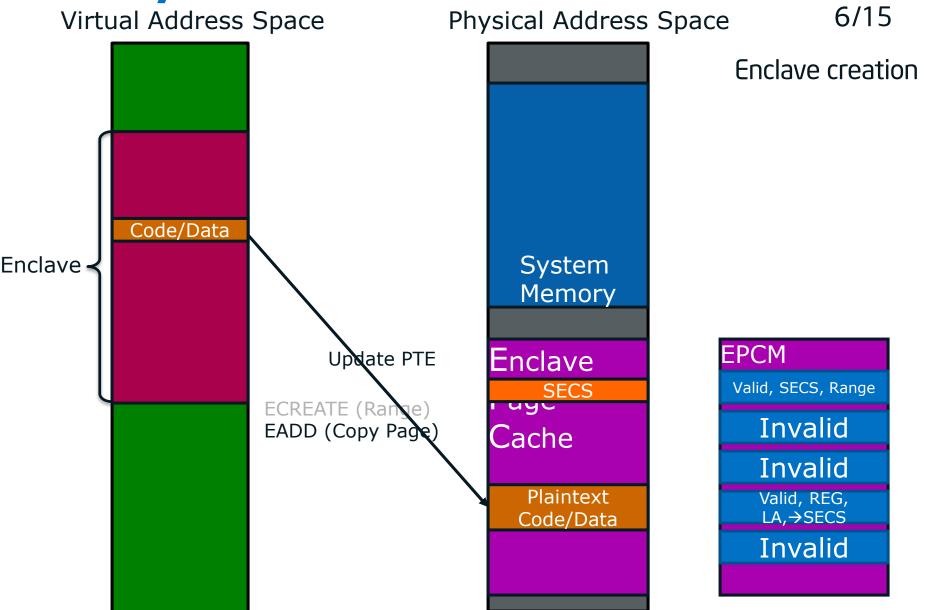




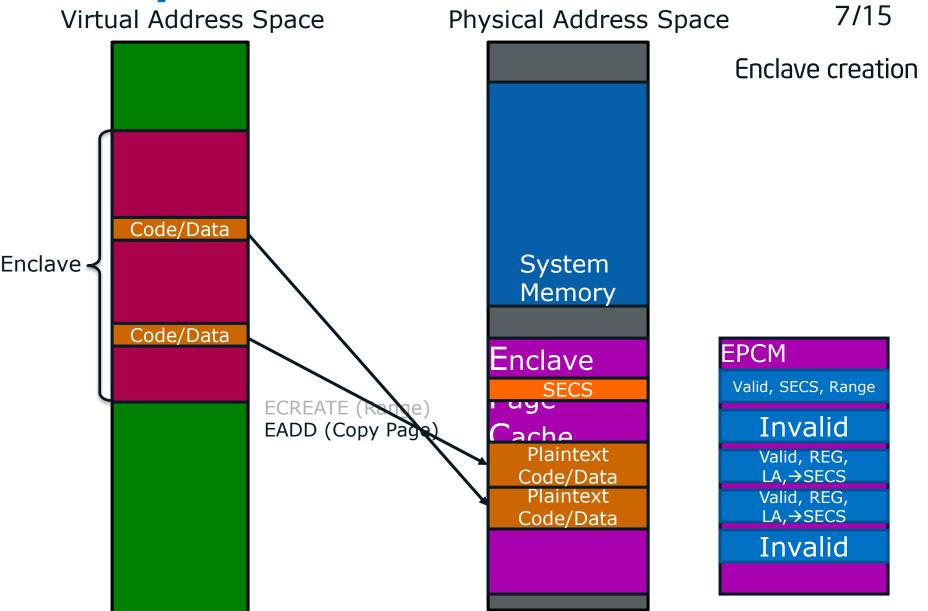




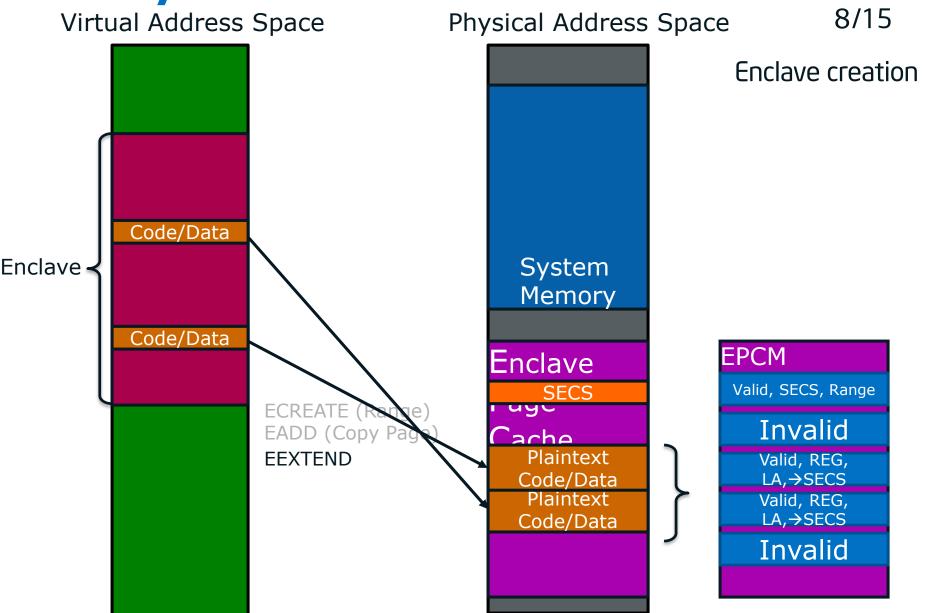




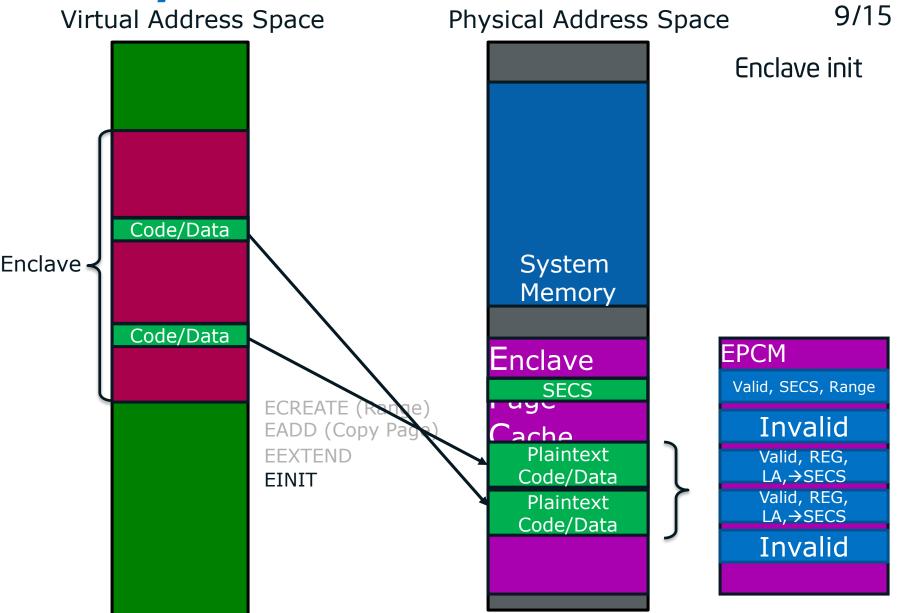




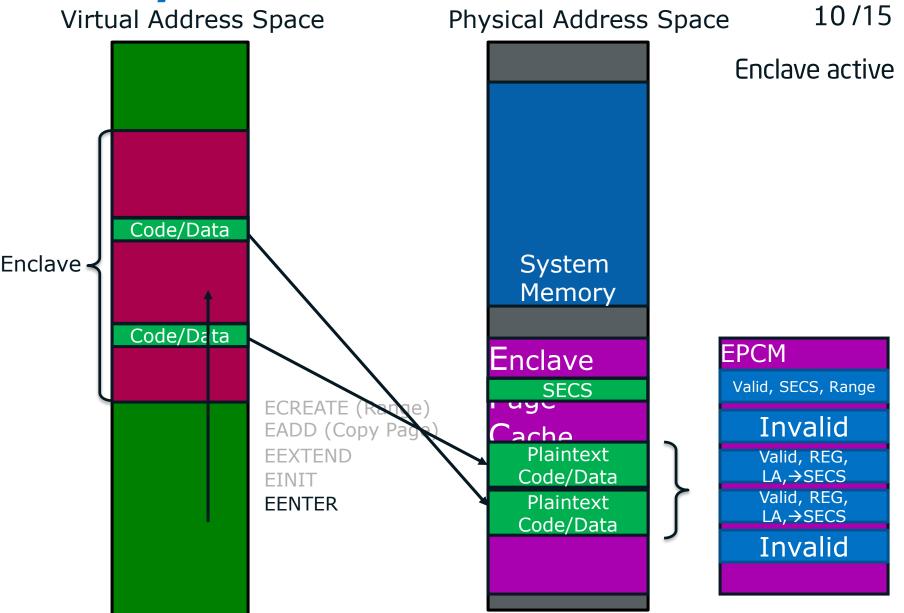




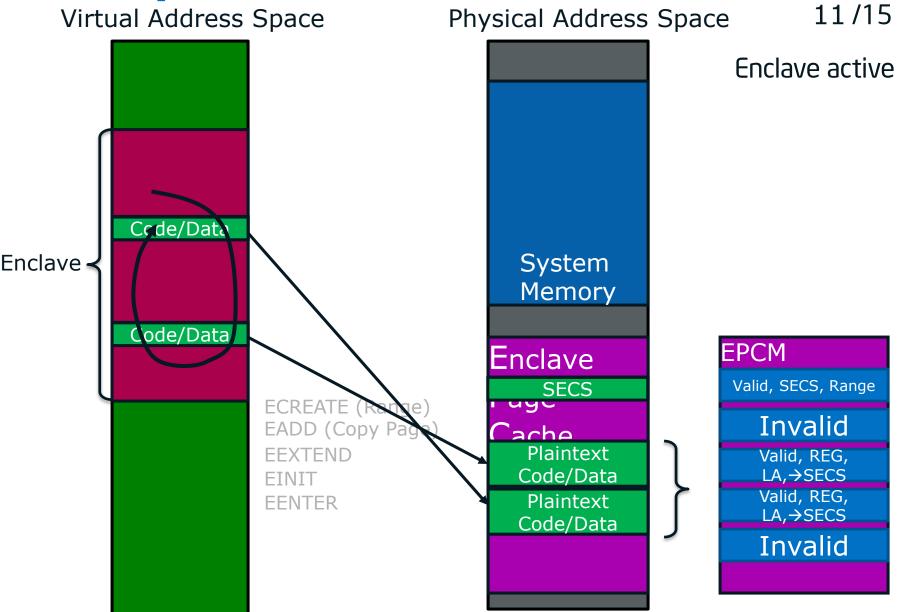




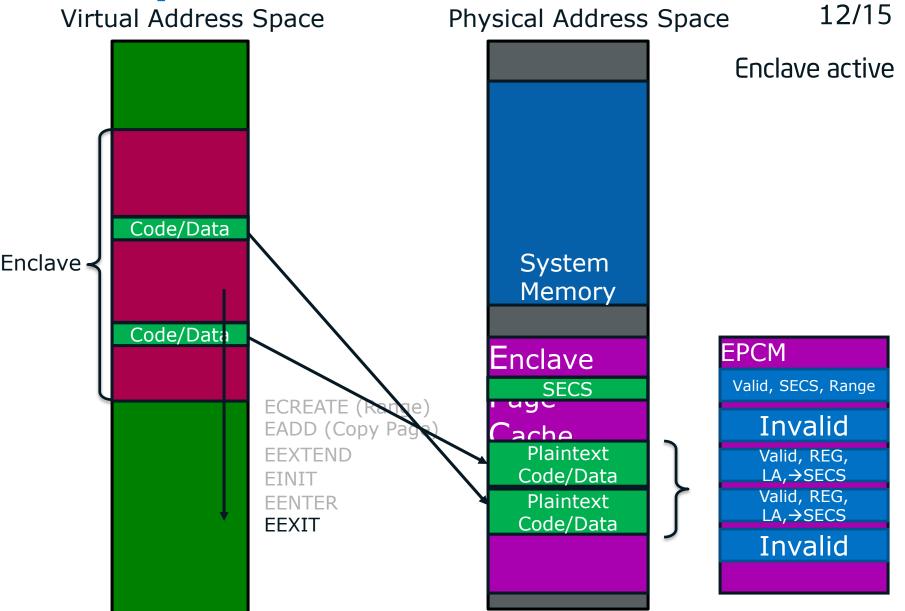




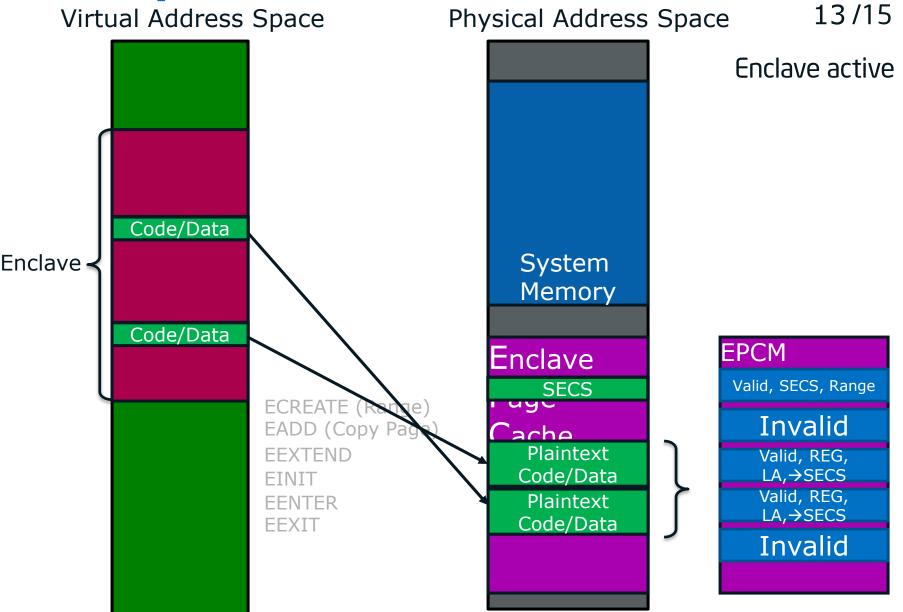




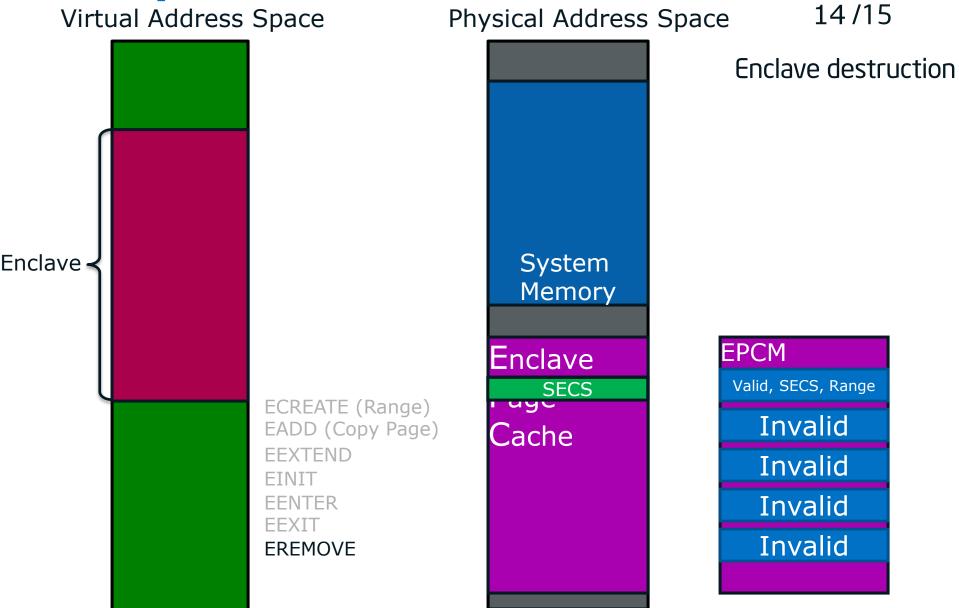










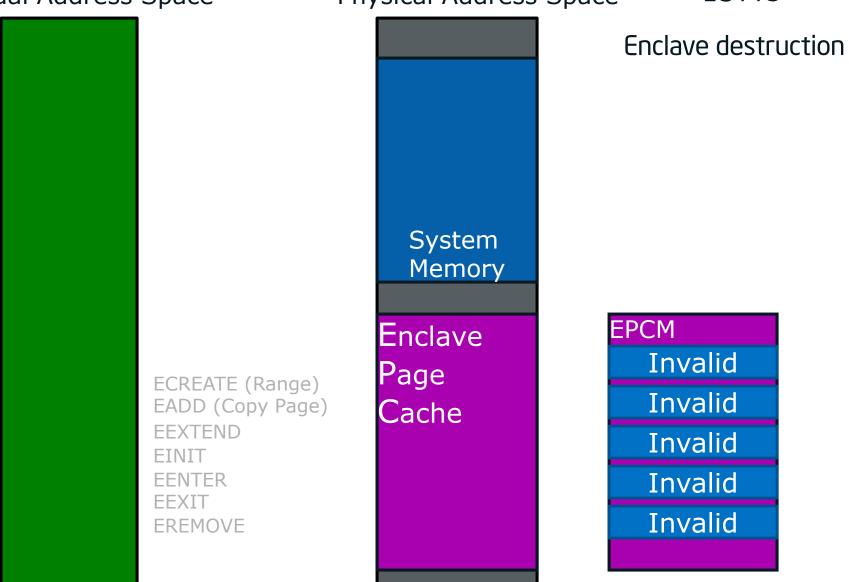




Virtual Address Space

Physical Address Space





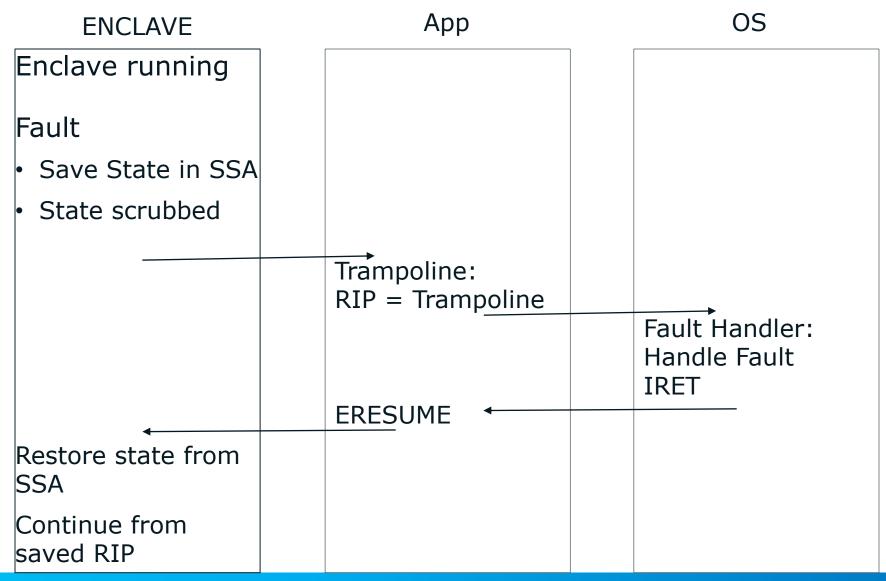


Handling Exceptions

- Asynchronous Exit (AEX)
 - Faults, exceptions and interrupts initiate the Asynchronous Exit flow.
 - During AEX, enclave register state is stored in the enclave's active SSA frame and initialized to a known value prior to leaving the enclave
 - The RIP is initialized to an area referred to as the trampoline code
- SSA
 - Each enclave thread has a dedicated State Save Area frame entry that is pre-defined by the ISV for that thread



Handling Exceptions





Attestation

SGX provides LOCAL and REMOTE attestation capabilities

Local attestation allows one enclave to attest its TCB to another enclave on the same platform

Remote attestation allows one enclave to attest its TCB to another entity outside of the platform



Sealing

"Sealing": Cryptographically protecting data when it is stored outside enclave.

Enclaves use EGETKEY to retrieve a persistent key that is enclave & platform specific

EGETKEY uses a combination of enclave attributes and platform unique key to generate keys

- Enclave Identity
- Enclave Sealing Authority & Product Identity

Enclave is responsible for performing the encryption with an algorithm of its choice.



Summary

- Intel® SGX provides outstanding data protection and a simple programming model
 - An enclave limits the size of the TCB
 - Enclaves are protected in face of a compromised OS/VMM.
- Developers may focus on securing the smaller TCB
- Enclaves run within the application process
 - be built and debugged with familiar tools.
- The Intel SGX SW stack and tools should simplify development even more.



Links

Joint research poster session: <u>http://sigops.org/sosp/sosp13/</u>

Public Cloud Paper using SGX2:

https://www.usenix.org/sites/default/files/osdi14_full_proceedi ngs.pdf

Programming Reference for SGX1 & SGX2: http://www.intel.com/software/isa

HASP Workshop: https://sites.google.com/site/haspworkshop2013/workshopprogram

ISCA 2015 Tutorial Link: http://sgxisca.weebly.com/





Thank You