QA Test Engineer – Design (position # 1963)

**Job Description:**

- Bug tracking
- Definition for internal testing tools
- Definition of test plan (Test Vs specs, Random tests, ATPS)

education:

BSC (or experienced practical engineer) or equivalent: SW / HW / Electrical

**Requirements:**

- 3+ years experience
- Knowledge in writing test plans/scenario based on HW/FW design documents
- Knowledge in Formal testing methodologies
- Knowledge in configuration control and bug tracking tools such as Accurev, or any other configuration tool
- knowledge of English

Please send CV’s to the following address:

sandisk_tefenCV@talent-sys.com

Mention your name in the subject line + position #