Open positions for CS graduates at Marvell

10-7161 Software Engineer (DT Group)

Description:
CAD/Verification Engineer for the Logic Technologies group.
Responsibilities: Development of verification tools for architectural modeling and test generation

Qualifications:
Education:
Required (minimal) • B.Sc Computer Science OR • Computer Engineering OR • EE with SW track (Academic Institutes only. No colleges or retraining programs)
Experience:
1.5+ years’ experience (industry or academy) Knowledge and proven experienced with:
• Object Oriented Design
• C++
• Java (Advantage)
• Eclipse IDE (Advantage)
• S/W engineering on Unix/Linux platform

Location: Yokneam

11-10764 Back End Engineer

Description:
Backend implementation of ASIC/CPU modules (RTL to GDS)
• Introduction and ramping of new tools
• Building new design flow for advanced technologies
Technical -
• Experience in backend of ASIC/VLSI - advantage
• Knowledge of Verilog - advantage
• Knowledge and experience with backend tools - advantage

Qualifications:
Education -
• B.Sc in Electrical / Computer Engineering - Mandatory
• Courses on backend design tools - Optional
• Course in RTL Verilog coding – Optional
Technical -
• Experience in backend implementation of ASIC/CPU – advantage
• Knowledge in Verilog - advantage
• Knowledge of Synopsys backend tools - advantage

Location: Petach Tikva
11-9443 Software Engineer - RT (Cellular)

Description:
The recruited Engineer will join L2 sub team and will take part in the development of high data rates transfer.

Qualifications:
Education:
MSc or BSc in computer science, computers engineering or electronic engineering
Experience: wide range of experience, including recently graduated. Preferred: *5 years
Technical knowledge:
Communication SW development; Embedded RT SW development; Cellular
Tools: Clear Case, ARM compiler, VS, Simulators
Programming language: C

Location: Petach Tikva

Please send your CS to: Marvell_jobs@talent-sys.com