Software Engineer (DT Group)

Description: CAD/Verification Engineer for the Logic Technologies group.
Responsibilities:• Development of verification tools for architectural modeling and test generation

Qualifications: 
Education: Required (minimal) • B.Sc Computer Science OR• Computer Engineering OR • EE with SW track (Academic Institutes only. No colleges or retraining programs)
Experience: 1.5+ years’ experience (industry or academy) Knowledge and proven experienced with:• Object Oriented Design• C++• Java (Advantage)• Eclipse IDE (Advantage)• S/W engineering on Unix/Linux platform

Location: Yokneam

Back End Engineer

Description: Backend implementation of ASIC/CPU modules (RTL to GDS)
• Introduction and ramping of new tools
• Building new design flow for advanced technologies

Technical -
• Experience in backend of ASIC/VLSI - advantage
• Knowledge of Verilog - advantage
• Knowledge and experience with backend tools - advantage

Qualifications: 
Education -
• B.Sc in Electrical / Computer Engineering - Mandatory
• Courses on backend design tools - Optional
• Course in RTL Verilog coding – Optional

Technical -
• Experience in backend implementation of ASIC/CPU - advantage -
• Knowledge in Verilog - advantage
• Knowledge of Synopsys backend tools - advantage

Location: Petach Tikva + Yokneam

Please send your CV: Marvell_jobs@talent-sys.com